



Kim, Hak Yong

김 학 룡

Assistant Professor, Soon Cheon Hyang University
CEO, IoT Strategy Labs

RESEARCH AREAS

Internet of Things (IoT) Technology
Digital Transformation Strategy
Methodology for Digital Service Design and Design Thinking
Platform Business and Digital Business Models

TEACHING AREAS

Digital Service Design and Strategy
Business Model
Strategy based on Internet of Things (IoT)
The 4th Industrial Revolution and Digital Transformation
5G Technology and 5G-based Business Development

EDUCATION

(2001) Ph.D in Information & Communication Engineering, GIST (Gwangju Institute of Science and Technology)
(1997) MS in Information & Communication Engineering, GIST
(1995) B.S. in Electronics Engineering, Chungnam National University

EXPERIENCE

(2016.3-Present) Professor, Soon Chun Hyang University
(2014.12-2016.2) Professor, Pusan National University
(2014.7-Present) CEO, IoT Strategy Labs
(2009.7-2014.4) General Manager, LG U+
(2003.9-2009.4) Deputy General Manager, Samsung SDS
(2001.12-2003.8) Senior Engineer, Corecess Inc.
(2001.8-2001.12) Research Professor, GIST
(2000.1-2000.2) Visiting Researcher, Communications Research Labs (CRL), Tokyo, Japan

PUBLISHED JOURNALS

- Double-Sided Two-Way Ranging Algorithm to Reduce Ranging Time, IEEE Communications Letters, vol. 13, no. 7, 2009
- Performance Analysis of the Multiple Input-Queued ATM Switch with the Restricted Rule, IEEE/ACM Tr. Networking, vol. 11, no. 3, 2003
- Evaluation of the Fairness of the Non-Independent System, IEICE Tr. Communications, vol. E84-B, no.11, 2001
- Hierarchical Scheduling Algorithm for QoS Guarantee in the MIQ Switches, IEE Electronics Letters, vol. 36, no. 18, 2000
- Derivation of the mean cell delay and cell loss probability for multiple input-queued ATM switches, IEEE Communications Letters, vol.4, no.4, 2000
- A Throughput-Enhanced Parallel Scheduling Algorithm for the MIQ Switch with a Moderate Number of Queues, IEE Electronics Letters, vol. 36, no. 5, 2000
- Fairness Concept in terms of the Utilization, IEE Electronics Letters, vol. 36, no. 4, 2000
- Throughput analysis of the bifurcated input-queued ATM switch, IEICE Tr. Communications, vol. E82-B, no.5, 1999
- Throughput analysis of the bifurcated input-queued switches with restricted contention, IEE Electronics Letters, vol. 34, no. 17, 1998

AUTHORED BOOKS

- 냉장고를 공짜로 드립니다 - 사물인터넷에서 시작되는 비즈니스 패러다임의 변화, 2019
- 포워드 2019 미래를 읽다, 2019
- NodeMCU로 시작하는 사물인터넷 DIY, 2017
- 4차 산업혁명과 빅뱅파괴의 시대, 2017
- IoT지식능력검정, 2015
- 사물인터넷 - 개념, 구현기술, 그리고 비즈니스, 2014